

BTC53S3 SERIES CMOS STRATUM 3 VC/TCXO - 5.0 x 3.2 x 1.2mm

Frequency Range	10.000MHz to 40.000MHz	
Frequency Stability	(Overall for 20 Years) vs. Temperature vs. Supply Voltage $\pm 5\%$ vs. Load	
Holdover Stability (24 Hours)	± 4.6 ppm max ± 0.5 ppm max (See Chart Below) ± 0.2 ppm max ± 0.2 ppm max ± 0.32 ppm max	
Supply Voltage $\pm 5\%$	3.3V	5.0V
Output	CMOS	
Output Load	15pF	
Output Level High "1" Output Level Low "0"	90% Vdd min	10% Vdd max
Symmetry	45% / 55%	
Current Consumption	6.0mA max	
Temperature Range	See Chart Below -55°C to +125°C	
Control Voltage	1.5V \pm 1.0V	1.5V \pm 1.0V
Tuning Range	± 5.0 ppm min	
Phase Noise @ 19.2MHz	100Hz offset 1kHz offset 10kHz offset	-125dBc/Hz -145dBc/Hz -150dBc/Hz
Tristate (Optional)	Disable Enable	0.3 Vdd max 0.7 Vdd min

FREQUENCY STABILITY vs. TEMPERATURE (■ - available) (▲ - conditional) (x - not available)

Temp °C / ppm	± 0.14	± 0.28	± 0.37	± 0.5
0 ~ +55	■	■	■	■
-10 ~ +60	■	■	■	■
-20 ~ +70	■	■	■	■
-30 ~ +85	▲	■	■	■
-40 ~ +85	x	▲	■	■

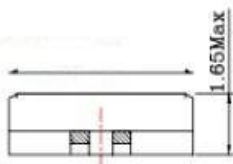
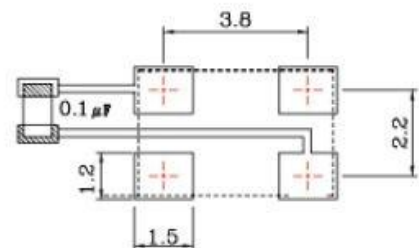
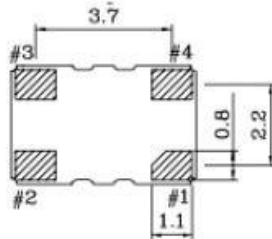
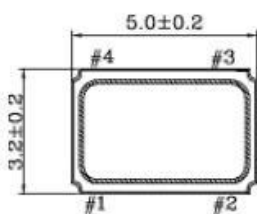
PART NUMBERING GUIDE

Series	Pin 1	Voltage	Stability vs. Temperature	Temp Range	Frequency
BTC53S3	Voltage Control = V N/C = Blank	3.3V = 33 5.0V = 50	± 0.5 = F ± 0.37 = G ± 0.28 = H ± 0.14 = K	0 ~ +55°C = 05 -10 ~ +60°C = 10 -20 ~ +70°C = 20 -30 ~ +85°C = 35 -40 ~ +85°C = 45	20M000

Example P/N: BTC53S3 – V – 33 – H – 35 – 20M000

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MECHANICAL DRAWING



Pin#	Function
1	VCON:VC-TCXO NC:TCXO *OPTION:Tri-State
2	GND
3	OUTPUT
4	VDD

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